

Sub: CO
Time: 20 minutes
Roll NO:

Date: 03-03-18
Max.Marks:10
Signature of Invigilator:

Answer all the bits, each bit carries 1/2 mark.

1. Which processor register communicate with memory_____ []
a)instruction register b)program counter c)R_i d)MDR
2. In addition operation overflow is detected by using_____ []
a) AND b)OR c)XOR d)NOT
3. Cache memory is used for _____ []
a)increase the performance b)decrease the performance c)both d)none
4. Auto increment mode is represented as_____ []
a)(R_i)+ b) (R_i- c)-(R_i) d) +(R_i)
5. stack pointer is used to indicate _____ []
a) bottom instruction b) top instruction c)both d)address of instruction
- 6.link register is used for _____ []
a)calling program b)subroutine c)return address d)all
7. The average time required to reach a storage location in memory and obtain its contents is called__ []
A. Latency time. B. Access time. C. Turnaround time. D. Response time.
8. In computers, subtraction is carried out generally by_____ []
a) 1's complement b).2's complement method c) signed magnitude d). BCD subtraction
9. Which of the following stores the next instruction address? []
a) Program counter b) Instruction Register c) Memory d) ROM
- 10) Which of the following is used to convert high level language to machine level language? []
a) Assembler b) Processor c) Compiler

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