

**Describe two best practices successfully implemented by the institution as per NAAC format provided in the manual**

The following are the two best practices of the institution that have contributed to the achievement of Institutional Objectives of the college.

Two Best Practices of the institution are:

1. Implementation of innovative methods in teaching & learning process
2. e-Governance

**BEST PRACTICE -1**

**Title of the Practice: Implementation of innovative methods in teaching – learning process**

**Goal of the Practice:**

- ❖ Driving the concepts home through project based learning approach
- ❖ Learning by doing
- ❖ Inculcating cognitive & computational thinking and programming capabilities
- ❖ Design and development of efficient and economical ways of solving real life problems

**Context:**

The current practice of paper based evaluation system in the middle and end of the semesters is not giving much scope to students to apply their concepts to develop a product or a service that solve a real life problem. There is a growing need for tools both hardware & software which will prompt the students to think innovately and come up with the solutions that are time and cost effective.

**Practice:**

Students who build circuits in their very first year of engineering using electronics workbenches can design circuits to address practical problems by the time they reach final year.

Programming has become an enigma for many engineering students. Using innovative software tools like programming workbench will motivate them to learn and apply code to solve practical problems.

Also, e-learning through Virtual labs, YouTube videos, lectures and other related sources make learning easy and pleasurable

**Evidence of success:**

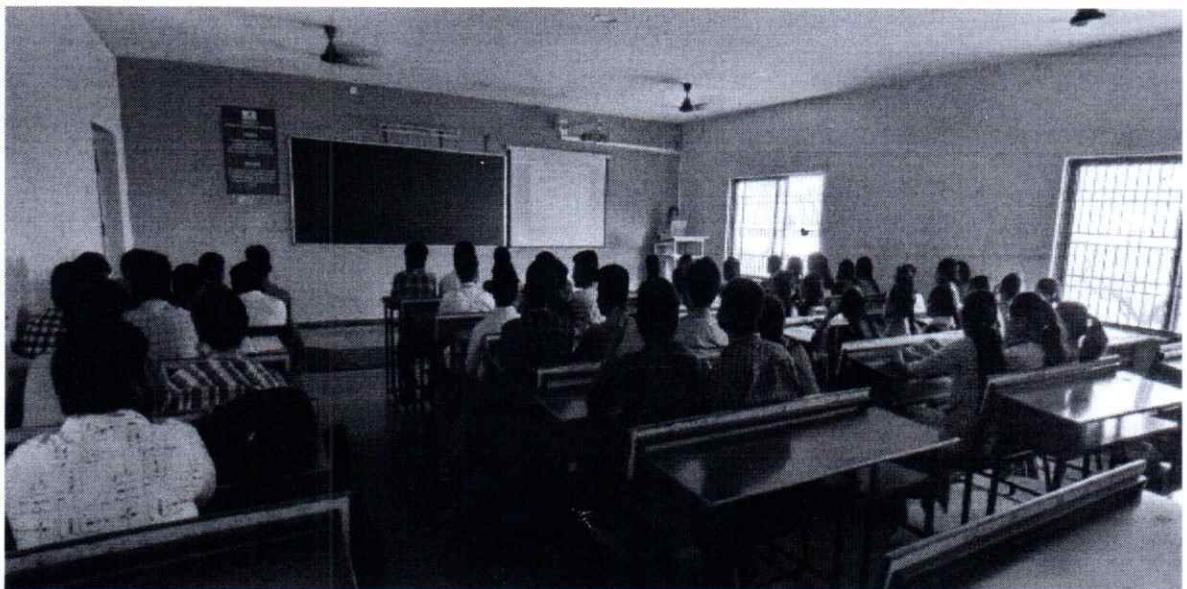
More and more students are participating in Coding and IOT design competitions. Students are developing IT & Engineering products for solving existing problems or for new applications.

**Problems encountered and Resources required**

It takes a lot of time and effort to get acceptance of teachers & students to embrace new technologies.

  
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1. Flipped Class Room:



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## 2. ICT Support Learning

### a) Microsoft Teams

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# Actuator

**Definition:**

- An Actuator is something that actuates or moves something.
- An Actuator is a device that converts the electrical signal to motion/force.

Actuators are almost always transducers but transducers are not necessarily Actuators.

A Transducer is a device that converts from one form of energy into another.

For example, a **microphone** (input device - Sensor) converts sound waves into electrical signals for the amplifier to amplify (a process), and a **loudspeaker** (output device - Actuator) converts these electrical signals back into sound waves.

"Transducer" is the collective term used for both Sensors and Actuators

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SK KP PS TP ST BS SK US BK MA P

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# Arduino UNO Board Parts

Digital Ground  
Analog Reference Pin  
Reset Button  
USB Plug  
Crystal Oscillator  
Voltage Regulator  
Power Jack  
7-12V DC Input  
2.1mm x 5.5mm

Digital I/O Pins  
Serial Out (TX)  
Serial In (RX)  
ATmega16u2 USB Controller  
Power Led Indicator  
In Circuit Serial Programmer  
ATmega328 Microcontroller

Reset Pin  
3.3V Pin  
5V Pin  
Voltage In Pin  
Ground Pin  
Analog Pins

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KA BR MH KK MN MA PS NS YK MR P

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b) NPTEL/ Swayam Videos

The screenshot shows the NPTEL website interface for the course 'Computer Networks and Internet Protocol'. The video player displays the title 'Lecture 18: Transport Layer Primitives' and a video frame with the text 'COURSE ON COMPUTER NETWORKS AND INTERNET PROTOCOL'. The video player controls show a progress bar at 0:05 / 28:15 - Chapters.

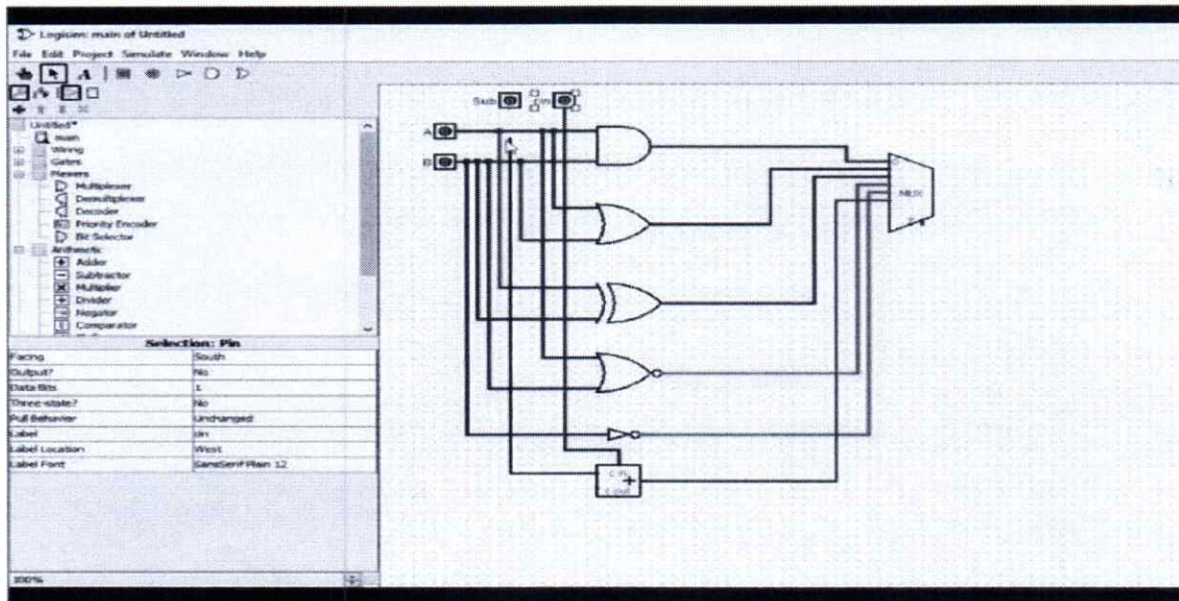
The screenshot shows the NPTEL website interface for the course 'Computer Networks and Internet Protocol'. The video player displays the title 'Lecture 18: Transport Layer Primitives' and a video frame with the text 'Transport Service Primitives'. Below the title, there is a bullet point: 'To allow users to access transport service, the transport layer must provide some operations to the application programs.' Below this is a table with columns: Primitive, Packet sent, and Meaning.

Primitive	Packet sent	Meaning
LISTEN	(none)	Block until some process tries to connect
CONNECT	CONNECTION REQ.	Actively attempt to establish a connection
SEND	DATA	Send information
RECEIVE	(none)	Block until a DATA packet arrives
DISCONNECT	DISCONNECTION REQ.	Request a release of the connection



c) Virtual labs

Logism software



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# GPCET

Pioneering Innovative Education

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year: 2020-21

Faculty Using ICT tools

S.No	Class	Topic Delivered using ICT	Subject	Name of the Faculty	ICT tools used
1	III	Basic Maxwell's equations	Antennas and Wave Propagation	Dr.KCTSwamy	<a href="https://www.youtube.com/watch?v=XUR-dnDa7el">https://www.youtube.com/watch?v=XUR-dnDa7el</a>
2	III	Antenna Arrays	Antennas and Wave Propagation	Dr.KCTSwamy	<a href="https://www.youtube.com/watch?v=mudiXpU_DVQ">https://www.youtube.com/watch?v=mudiXpU_DVQ</a>
3	III	Pulse code modulation	Digital Communication Systems	Mrs.Fowziasulthana	<a href="https://www.youtube.com/watch?v=l_SqkcP6hZ4">https://www.youtube.com/watch?v=l_SqkcP6hZ4</a>
4	III	Introduction Convolution Codes	Digital Communication Systems	Mrs.Fowziasulthana	<a href="https://www.youtube.com/watch?v=AnyVu5eDhAQ">https://www.youtube.com/watch?v=AnyVu5eDhAQ</a>
5	III	FET	Digital Signal Processing	T. Suman	<a href="https://www.youtube.com/watch?v=r4Pc-rGBRJA">https://www.youtube.com/watch?v=r4Pc-rGBRJA</a>
6	III	Discrete Time Signal & system	Digital Signal Processing	T. Suman	<a href="https://www.youtube.com/watch?v=8zbBJkw5KsM&amp;list=PL4FA894BD6A9586E1">https://www.youtube.com/watch?v=8zbBJkw5KsM&amp;list=PL4FA894BD6A9586E1</a>
7	III	Logic Gates	CMOS VLSI Design	Dr. Sandipwarnakar	<a href="https://www.youtube.com/watch?v=ft3TC2AU_H8">https://www.youtube.com/watch?v=ft3TC2AU_H8</a>
8	III	Design of Adders	CMOS VLSI Design	Dr. Sandipwarnakar	<a href="https://www.youtube.com/watch?v=85XxQZqBNlg">https://www.youtube.com/watch?v=85XxQZqBNlg</a> <a href="https://www.youtube.com/watch?v=YBY0k5mIQy0">https://www.youtube.com/watch?v=YBY0k5mIQy0</a>
9	IV	Photo Detector	Optical Fiber Communication	K.Anil Kumar	<a href="https://www.youtube.com/watch?v=1X2Xt7wlcBA">https://www.youtube.com/watch?v=1X2Xt7wlcBA</a>
10	IV	Attenuation & Power loss in fibers	Optical Fiber Communication	K.Anil Kumar	<a href="https://www.youtube.com/watch?v=FVHGrGRN6i8">https://www.youtube.com/watch?v=FVHGrGRN6i8</a>

## G.PULLAIAH COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous)

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11	IV	Gunn diode. Varactor diode	Microwave Engineering Digital Image Processing	K.Anil Kumar & T.Suman	<a href="https://www.youtube.com/results?search_query=Gunn+diode-principles+classes+by+iit+professors">https://www.youtube.com/results?search_query=Gunn+diode-principles+classes+by+iit+professors</a>
12	IV	Directional Couplers	Microwave Engineering Digital Image Processing	K.Anil Kumar & T.Suman	<a href="https://www.youtube.com/watch?v=44mBn7nyMIM">https://www.youtube.com/watch?v=44mBn7nyMIM</a>
13	IV	Edge Detection	Digital Image Processing	T.Suman	<a href="https://www.youtube.com/watch?v=9r8ph2pb9aw">https://www.youtube.com/watch?v=9r8ph2pb9aw</a>
14	IV	Discrete Cosine Transforms	Digital Image Processing	T.Suman	<a href="https://www.youtube.com/watch?v=S8FkaEWfCOg">https://www.youtube.com/watch?v=S8FkaEWfCOg</a>
15	IV	Transmission media reflection	RF Integrated Circuits	Mrs.S.Fowziasulthana	<a href="https://www.youtube.com/watch?v=q9k9FPh0iJI&amp;list=PLbMVogVj5nJQdGDSx243YPnNeLMBrhNE8&amp;index=2">https://www.youtube.com/watch?v=q9k9FPh0iJI&amp;list=PLbMVogVj5nJQdGDSx243YPnNeLMBrhNE8&amp;index=2</a>
16	IV	RF system basic architectures	RF Integrated Circuits	Mrs.S.Fowziasulthana	<a href="https://www.youtube.com/watch?v=KUDGGsyh1Hs">https://www.youtube.com/watch?v=KUDGGsyh1Hs</a>
17	II	Sample space & Events	Probability Theory and Stochastic Processes	Dr. N.Ramamurthy	<a href="https://www.youtube.com/watch?v=leVm6xuKdlU">https://www.youtube.com/watch?v=leVm6xuKdlU</a>
18	II	Random Variables	Probability Theory and Stochastic Processes	Dr. N.Ramamurthy	<a href="https://www.youtube.com/watch?v=zujeySyREcQ4">https://www.youtube.com/watch?v=zujeySyREcQ4</a>
19	II	Zener Diode and applications	Electronic Devices and Circuits	Dr.T.Tirupal	<a href="https://www.youtube.com/watch?v=I2nrBITEVIs">https://www.youtube.com/watch?v=I2nrBITEVIs</a>
20	II	BJT biasing- methods	Electronic Devices and Circuits	Dr.T.Tirupal	<a href="https://www.youtube.com/watch?v=l6M6FvjUdTI">https://www.youtube.com/watch?v=l6M6FvjUdTI</a>
21	II	Vestigial sideband (VSB) modulator	Analog Communication Systems	Dr.T.Tirupal	<a href="https://www.youtube.com/watch?v=KC8kh5Falc0">https://www.youtube.com/watch?v=KC8kh5Falc0</a>
22	II	Amplitude modulation	Analog Communication Systems	Dr.T.Tirupal	<a href="https://www.youtube.com/watch?v=UznnkHMislk">https://www.youtube.com/watch?v=UznnkHMislk</a>

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Principal  
HOD-ECE

## **BEST PRACTICE -2**

Title of the Practice: **e- Governance**

### **Goal of the Practice:**

The goal of the practice is to provide effective and easy way to automate the functionalities of the Institute and to provide information about the students and staff to the stake holders by way of Digital Campus and college website. Digital campus software which is being used by our institute provides various types of educational related services like maintaining attendance and internal marks, work load of staff members, leave history, mentoring data etc, thereby reducing human error and paper work. The college website **www.gpcet.ac.in** provides information about the various departments of the institution, vision, mission, placement details, facilities provided etc.

### **Context:**

Our institute has a total annual intake of 816 UG students in 7 UG and 4 PG programs. To maintain the records of all these students manually is a huge task and there are chances of committing errors. Hence Digital Campus software is being used by our Institute to store information about various types of educational related services like maintaining attendance and internal marks, work load of staff members, leave history, mentoring data etc, thereby reducing human error and paper work.

### **Practice:**

Digital campus software is loaded with main features like Admissions and Academics, correspondence, library, administration etc., by which the details of attendance and internal marks, work load of staff members, leave history, backlogs of students, mentoring data etc., which can be accessed when necessary by both the staff as well as student using their Login Id and passwords. To access to the software can be done through the following link: <https://digitalcampus.msmfclasses.com:90>

### **Evidence of Success**

- ❖ The information regarding any student or staff can be accessed in minimum amount of time.
- ❖ The analysis of marks and attendance can also be done easily.
- ❖ As the log-in access is also given to the parents, the parents are being able to access the information about attendance, marks etc., of their wards in minimum amount of time.
- ❖ Effective management of resources.
- ❖ Interaction between the faculty and students has also increased
- ❖ The information is stored centrally and no redundant data is stored

### **Problems encountered and Resources required**

- ❖ There will be problem in accessing the data when the network is down.
- ❖ There is a chance of files getting corrupted.

A separate high configuration server is to be allotted for this software.

  
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### **E-Governance Screen Shots Login Sheets:**

G.Pullaiah College of Engineering and Technology has ERP software which covers following areas of operation.

The screenshots of e-governance software are as follows:

1. Administration
2. Finance and Accounts
3. Student Admission and Support
4. Examination

#### **INDEX**

<b>S.no</b>	<b>E-Governance areas</b>	<b>Screenshot Description</b>
1	Administration	ERP home page
		ERP login page
		ERP modules
2	Finance and Accounts	ERP home page(Tally)
		ERP login page(Tally)
3	Student Admission and support	ERP login page(Digital campus)
		ERP modules(Digital campus)
		ERP home page(gpct.ac.in)
		ERP Modules(gpct.ac.in)
4	Examination	ERP Login Page
		ERP GPCET Portal

  
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# Concept (Bio Metric)

eSSL eTimeTrack Lite 10.0

Admin Masters Utilities Attendance Reports Canteen Monitor Windows Help

eSSL eTimeTrack Lite Login

eTimeTrack - Lite

Login Name:

Password:

Login Close

[Database Settings](#)

Note: This software is protected by copyright law and international treaties. Unauthorized reproduction or distribution of this program, or any portion of it, may result in severe civil and criminal penalties, and will be prosecuted under the maximum extent possible under law.

Login User: Login User Role: Role Login Time: Login Time

Powered By: <http://www.esslindia.com>

11:55 04-03-2022

Monthly Status Report

Monthly Status Report (Basic Work Duration)

Feb 01 2022 To Feb 28 2022

Company: GPCET Printed On: Mar 04 2022 11:58

Days	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31
Department: CSE																															
Emp. Code: 801	Emp. Name: Dr S Prash Kumar																														
Status	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30
Cuttime	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48	18:48
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Emp. Code: 802	Emp. Name: Dr K Sreenivasulu																														
Status	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	
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Total	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18	10:18
Emp. Code: 823	Emp. Name: Arisana Vasireen																														
Status	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	
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Department: H&S																															
Emp. Code: 804	Emp. Name: Dr T Byasa Jeevani Basu																														
Status	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	08:30	
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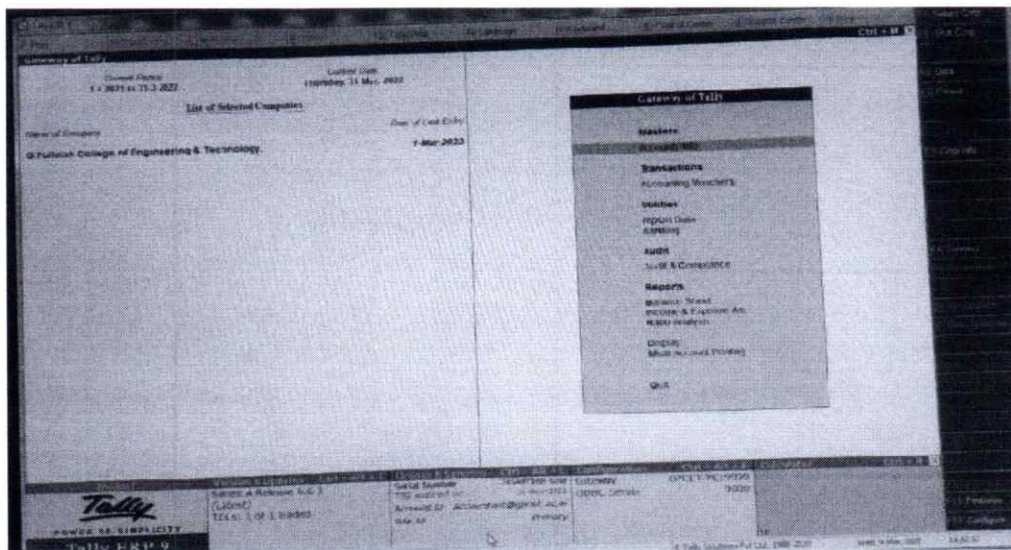
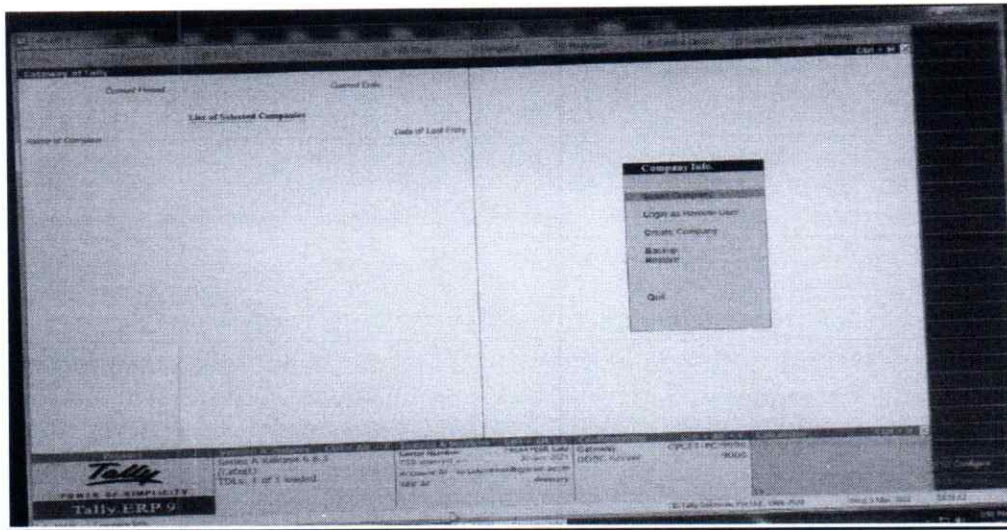
Concept-Home page & Login page

*e-India*

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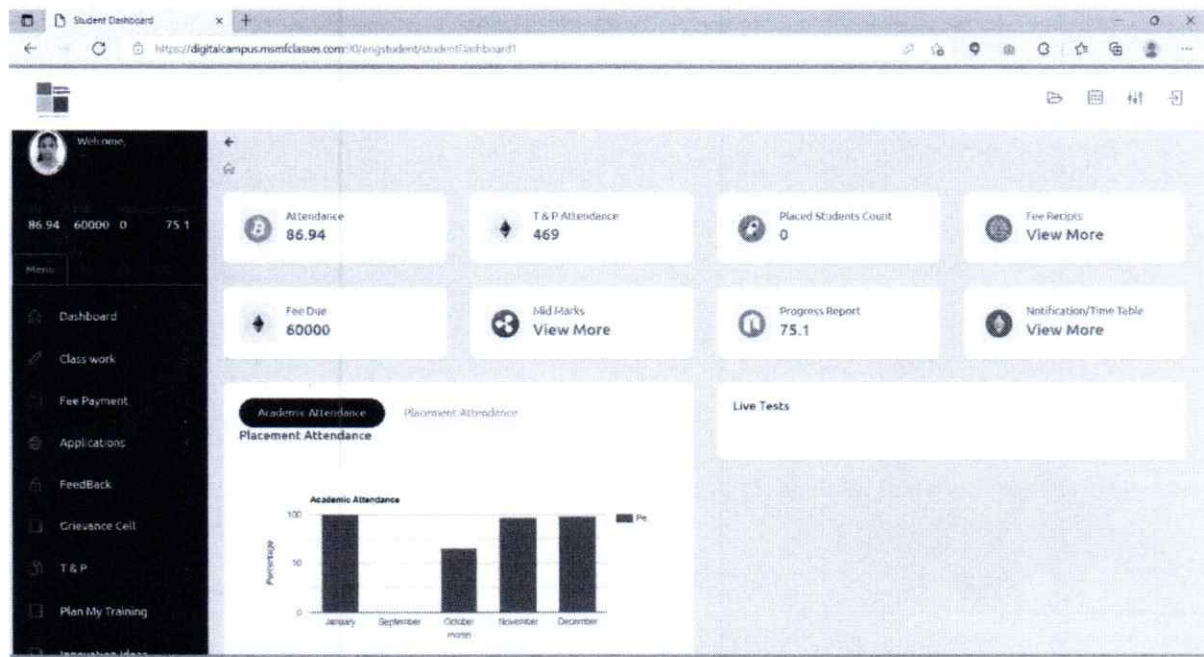
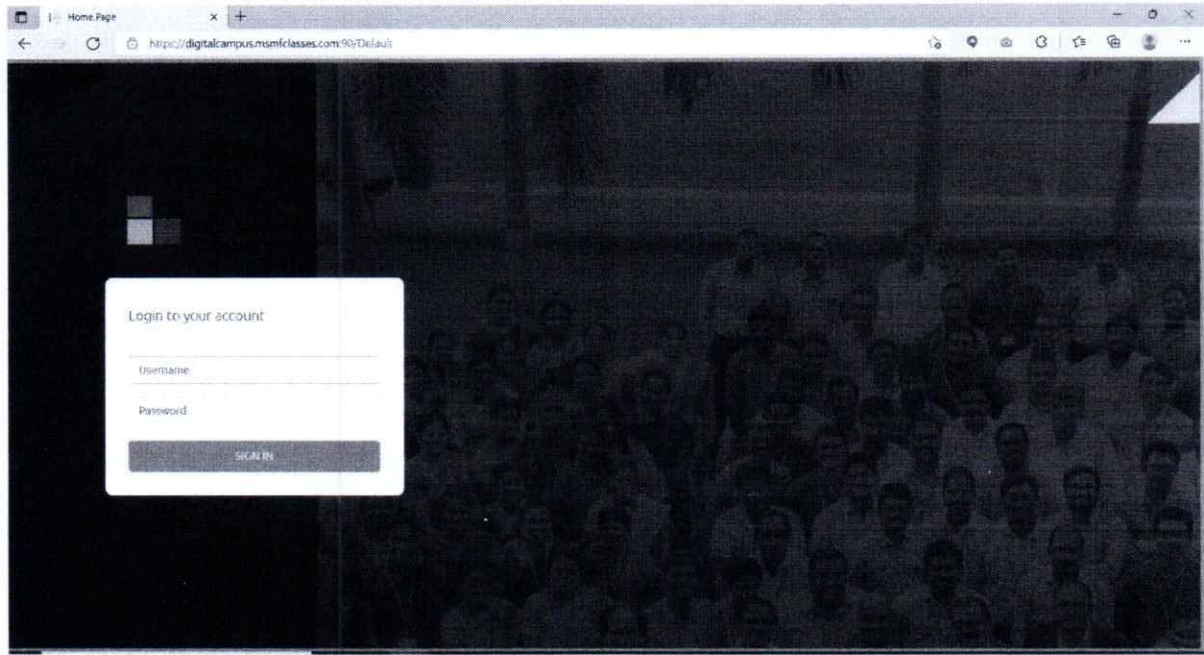
## Tally software



## Tally-Homepage

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## GPCET software



## Digital campus-HOME PAGE

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Leave Status

https://digitalcampus.msmfclasses.com:3000/mgrcollege/LeaveApplyStatus

Welcome, Pipshitha

0.86 93.1 2.0

Home > Academics > Leave > Leave Report > Leave Apply Status

Academic Financial Status Results

For Reports

Leave Status

SNO	STATUS
1	Your Leave Has Been Approved on Mon,28-Feb 02,2022 For CasualLeave,Full Day Your Leave Approved on Wed,02-Mar 03,2022 12-11-36
2	Your Leave Has Been Approved on Thu,04-Jan 01,2022 For CasualLeave,Full Day Your Leave Approved on Thu,04-Jan 01,2022 10-42-27
3	Your Leave Has Been Approved on Tue,28-Dec 12,2021 For CasualLeave,Full Day Your Leave Approved on Tue,21-Dec 12,2021 15-42-33
4	Your Leave Has Been Approved on Mon,27-Dec 12,2021 For CasualLeave,Full Day Your Leave Approved on Tue,21-Dec 12,2021 15-42-31
5	Your Leave Has Been Approved on Fri,24-Dec 12,2021 For CasualLeave,Full Day Your Leave Approved on Tue,21-Dec 12,2021 15-42-28
6	Your Leave Has Been Approved on Thu,23-Dec 12,2021 For CasualLeave,Full Day Your Leave Approved on Tue,21-Dec 12,2021 15-42-25
7	Your Leave Has Been Approved on Wed,22-Dec 12,2021 For CasualLeave,Full Day Your Leave Approved on Tue,21-Dec 12,2021 08-57-03
8	Your Leave Has Been Approved on Mon,08-Nov 11,2021 For CasualLeave,Full Day Your Leave Approved on Mon,22-Nov 11,2021
9	Your Leave Has Been Approved on Mon,08-Nov 11,2021 For CasualLeave,Full Day Your Leave Approved on Mon,08-Nov 11,2021

Financial Status Dashboard

https://digitalcampus.msmfclasses.com:3000/mgrcollege/yourFinancialStatusDashboard

Welcome, Pipshitha

0.86 93.1 2.0

Home > Dashboard > Faculty Dashboard > Your Financial Status Dashboard

Academic Financial Status Results

Biometric Attendance Absent View More

Leaves Availed 2.0 View More

Course Files View More

View More

Pay Slip

Show 10 entries Search

SNO	FACULTYID	EMPLOYEE NAME	MONTH	YEAR	ACTION
1	ECE1ATR2	P ipshitha	September	2021	View More
2	ECE1ATR2	P ipshitha	August	2021	View More
3	ECE1ATR2	Ms. P. ipshitha	January	2021	View More
4	ECE1ATR2	Ms. P. ipshitha	December	2020	View More
5	ECE1ATR2	P ipshitha	November	2020	View More
6	ECE1ATR2	Ms. P. ipshitha	January	2020	View More

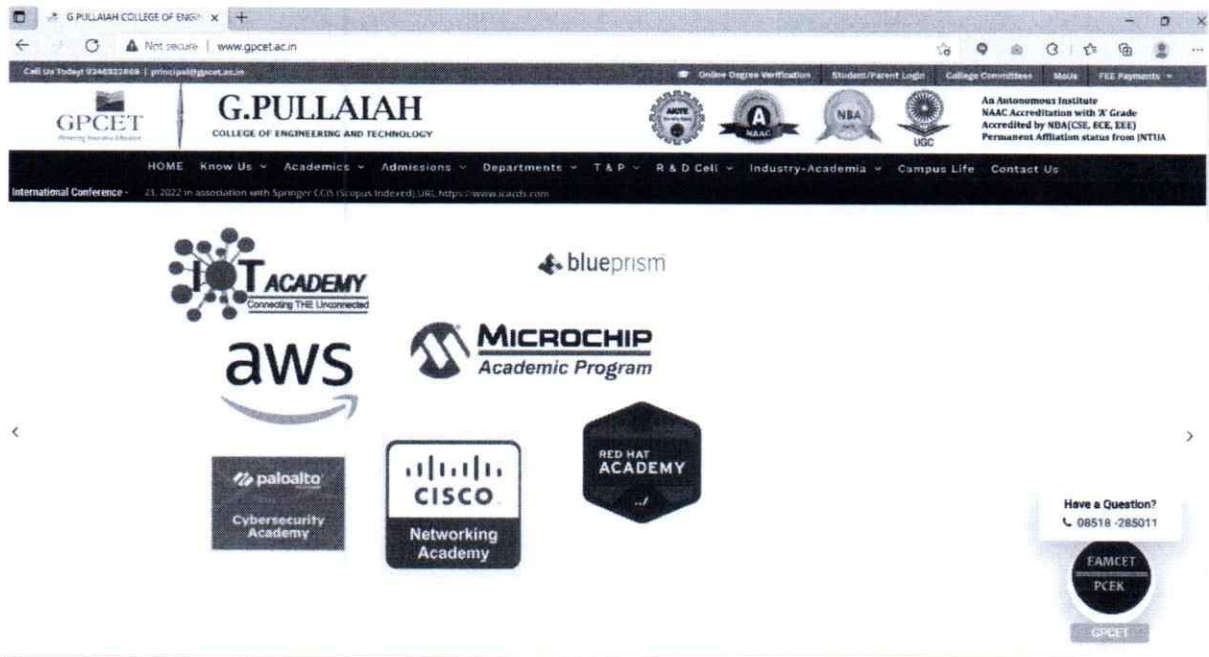
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**Dr.C.Srinivas Rao**  
Ph.D

Overview Qualifications Subjects Taught Publications

Dr C.Srinivasa Rao is currently working as Professor and Principal, G.Pullaiah college of Engineering & Technology, Kurnool. He has about 14 years of experience in teaching and research. He has completed his graduation in the year 2002 and his Post graduation from Birla Institute of Technology, Mesra in the year 2004 with Electrical Power Systems as Specialization. He has obtained his Ph.D from J.N.T.U Kakinada in the year 2010. He has published about 40 papers in International and National Journals and Conferences. He is also an author of 2 books titled "Electromagnetic Fields" and "Network Analysis" published by NewAge Publishers and Cengage Learning respectively. He has also delivered good number of Expert lectures in various workshops organised by different Engineering Colleges. His Research interests include Application of Soft Computing techniques in Power Systems.

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**Dr.C.Srinivas Rao**  
Ph.D

Overview Qualifications Subjects Taught Publications

- C.Srinivasa Rao, "Cost Reduction in clustering based unit commitment employing Hybrid Genetic-Simulated Annealing Technique" Accepted for publication in Journal of Electrical Engineering & Technology, January 2019.
- C.Srinivasa Rao, "Implementation of Additive and Divisive clustering based unit commitment employing Particle Swarm Optimization" International Journal of Applied Engineering Research, Volume 13, Number 10 (2018)pp. 8131-8138.
- C.Srinivasa Rao, "Implementation of Genetic Algorithm Based Additive and Divisive Clustering Techniques for Unit Commitment" 1st International Conference on Power Engineering, Computing and Control, PECCON-2017, 3-4 March 2017, VIT University, Chennai Campus, Energy Procedia(Elsevier), 117 (2017), pp 493-500.
- C.Srinivasa Rao, "Additive and Divisive Cluster Algorithm for Cost Reduction in Unit Commitment" Acta Electrotechnica et Informatica, Vol.16, No.4 (2016), pp 45-51
- C.Srinivasa Rao, "Implementation of clustering based Unit Commitment employing imperialistic competition algorithm" Electrical power and energy systems (Elsevier) 82(2016), 621-628.
- C.Srinivasa Rao et al, "Random Discontinuous PWM Schemes for VSI Fed AC Drive for Reduction of Harmonic Distortion, Switching Loss and Acoustic Noise," International Journal of Engineering Science & Research Technology (IJESRT), ISSN (Online): 2277-9855, Volume 5, Issue 11, November 2016, pp. 92-105, DOI: 10.5281/zenodo.164907.
- C.Srinivasa Rao et al, "Space Vector Based High Performance Discontinuous Pulse Width Modulation Algorithms for VSI Fed AC Drive" Innovative Systems Design and Engineering (ISDE), ISSN 2222-1727 (Paper) ISSN 2223-2871 (Online), Volume 7, Issue 7, July 2016, pp. 31-40.
- C.Srinivasa Rao et al, "Digital scalar pulse width modulation Methods for voltage source inverter Fed AC Drive" International Journal of Scientific Research Engineering

## GPCET-Modules

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- Processing and announcement of results.
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- Academic Calendar for III B.Tech I Sem - 2021-22
- Academic Calendar for II B.Tech I Sem - 2021-22
- Academic Calendar for I B.Tech I Sem - 2021-22

**M.Tech Program**

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- Academic Calendar for I M.Tech I Sem - 2021-22

**MBA Program**

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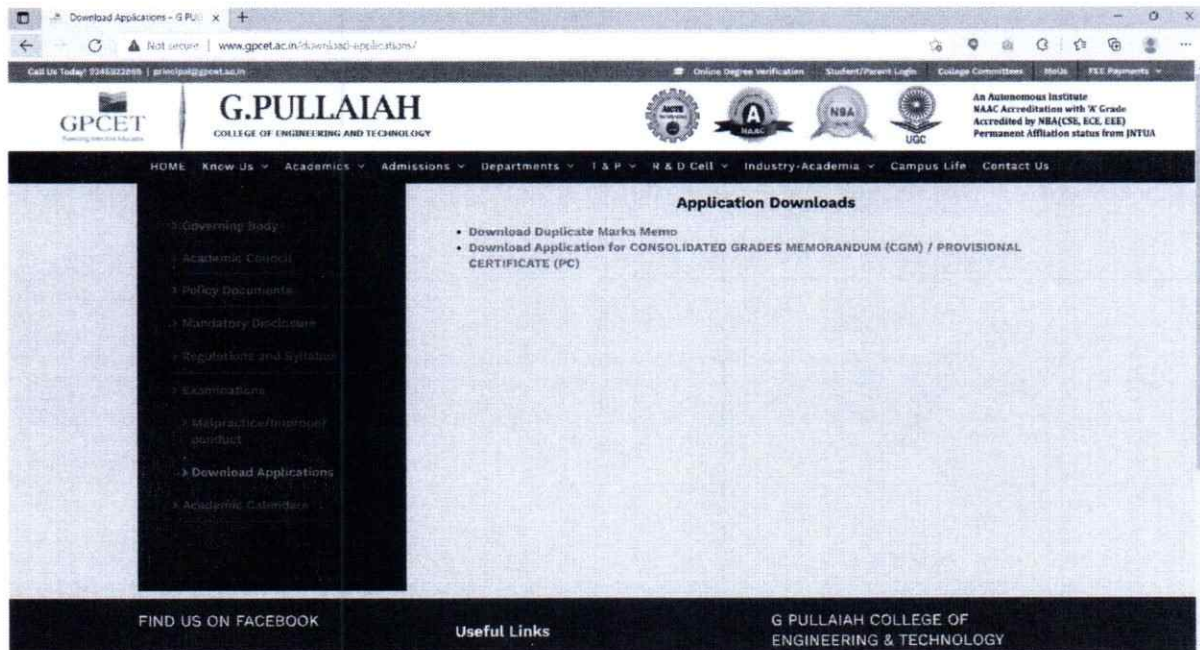
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