GPCET
Pioneering Innovative Education

SET 1

G.PULLAIAH COLLEGE OF ENGINEERING & TECHNOLOGY

III B.Tech. I Sem., I MID objective Exam

Branch: CSE

SUB: MPI **Date: 03-09-2016**

TIME: 20Min **Max Marks: 10M**

Answer the following:

1. The microprocessor 8008 was introduced in the year….. [ ]
   a. 1971 b. 1972 c. 1973 d. None
2. What is the size of address bus in 8085? [ ]
   a. 16 bit b. 20 bit c. 32 bit d. None
3. What is the length of instruction queue in 8086? [ ]
   a. 4 KB b. 4 bytes c. 6 bytes d. None
4. The 8085 microprocessor has……. Address lines. [ ]
   a. 12 b. 20 c. 16 d. None
5. The memory capacity of even memory bank of 8086 [ ]
   a. 512kb b. 1024kb c. 64kb d. None
6. Which assembler directive is used to end a segment? [ ]
   a. END b. ENDS c. ENDP d. None
7. STD……. [ ]
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12. _____ pin in 8086 decides the mode of operation of 8086.
13. _____ instruction is used complement the contents of accumulator in 8085.
14. If the microprocessor has 30 address lines then it contains------- memory locations.
15. The operating frequency of 8086 is ----------.
16. Expand MASM _____.
17. ---------- instruction is used to return to subprogram to main program.
18. RLC is a ---------- byte instruction.
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