GPCET
Pioneering Innovative Education

**SET 1**

**G.PULLAIAH COLLEGE OF ENGINEERING & TECHNOLOGY**
**II B.Tech. II Sem., I MID objective Exam**
**Branch: CSE**

**SUB: MPI** **Date: 09.03.2017**
**TIME: 20Min** **Max Marks: 10M**

**Answer the following:**

1. The microprocessor 4004 was introduced in the year….. [ ]
   a. 1971 b. 1972 c. 1973 d. None
2. What is the size of address bus in 8086? [ ]
   a. 16 bit b. 20 bit c. 32 bit d. None
3. What is the length of instruction queue in 8086? [ ]
   a. 4 KB b. 4 bytes c. 6 bytes d. None
4. The 8085 microprocessor has……. Hardware interrupts. [ ]
   a. 5 b. 7 c. 4 d. None
5. The memory capacity of 8086 is…… [ ]
   a. 512kb b. 1024kb c. 64kb d. None
6. Which assembler directive is used to define a byte type variable? [ ]
   a. DW b. DQ c. DB d. None
7. If CLD instruction is executed then DF= [ ]
   a.0 b. 1 c. a & b d. None
8. In order to perform signed division-------- instruction is used. [ ]
   a. DIV b. MDIV c. IDIV d. None
9. Find the Effective address if DS = 1BF5H and BX = 6203 H [ ]
   a. 7DF8H b.7DF80H c. 7FD80H d. None
10. What is the size of offset address in 8086-------- [ ]
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11. MOV AX, [SI] is an example of ----------addressing mode.
12. _____ pin in 8086 decides the mode of operation of 8086.
13. _____ instruction in 8085 is used load the contents of accumulator into memory location.
14. If the microprocessor has 22 address lines then it contains------- memory locations.
15. ---------- flag is set if the carry forwarded from lower nibble to upper nibble.
16. Expand CMA _____.
17. ---------- instruction is used to return to ISR to main program.
18. MOV A, M is a ---------- byte instruction.
19. ---------- technique is used to reduce the no of pins in microprocessors.
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